

RICK D. NYDEGGER
BRENT P. LORIMER
THOMAS R. VUKSINICK
LARRY R. LAYCOCK
JONATHAN W. RICHARDS
DAVID R. WRIGHT
JOHN C. STRINGHAM
JOHN M. GUYNN
CHARLES L. ROBERTS
DANA L. TANGREN
ERIC L. MASCHOFF
CHARLES J. VEVERKA
ROBYN L. PHILLIPS
RICHARD C. GILMORE *
STERLING A. BRENNAN
R. BURNS ISRAELSEN
DAVID R. TODD
GREGORY M. TAYLOR
DAVID B. DELLENBACH
L. DAVID GRIFFIN
ADRIAN J. LEE
FRASER D. ROY
CARL T. REED



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JOSEPH G. PIA
CLINTON E. DUKE
DAVID J. LORENZ †
RYAN N. FARR *
JAMES B. BELSHE
ALISON B. MOHR

VERNON R. RICE §
OF COUNSEL

WORKMAN | NYDEGGER

INTELLECTUAL PROPERTY ATTORNEYS

1000 EAGLE GATE TOWER
60 EAST SOUTH TEMPLE
SALT LAKE CITY, UTAH 84111
TELEPHONE: (801) 533-9800
FAX: (801) 328-1707
WEBSITE: HTTP://WWW.WNLAW.COM

PATENT
Docket No. 11675.165.2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Zhiping Yin, Mark Jost

Serial No.:

09/651,386

Filed:

August 29, 2000

For:

METHOD OF MAKING AN ELECTRICAL
DEVICE INCLUDING AN INTERCONNECT
STRUCTURE

Examiner:

T. N. Quach

Patent No.:

6,790,762 B1

Issue Date:

September 14, 2004

Certificate
DEC 07 2004
of Correction

REQUEST FOR A CERTIFICATE OF CORRECTION UNDER 37 C.F.R. § 1.323

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. § 1.323 which implement 35 U.S.C. § 255, the Commissioner is respectfully requested to issue a Certificate of Correction to correct mistakes in the above-identified patent. The enclosed Patent Office Form 1050 reflects the desired corrections. Also enclosed please find a check in the amount of \$100.00 to cover the costs for issuance of this certificate.

12/14/2004 HNGUYEN1 00000070 6790762

01 FC:1811

100.00 OP

* Admitted only in California
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§ Admitted only in Virginia

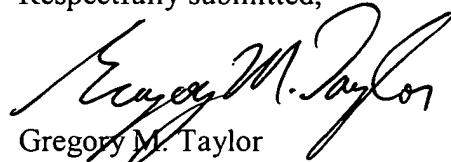
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The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 23-3178. Duplicate copies of this letter are enclosed.

Dated this 15th day of December, 2004 .

Respectfully submitted,

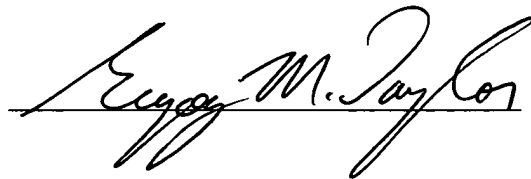


Gregory M. Taylor
Attorney for Patentee
Registration No. 34,263
Customer No. 022901

GMT:jjb
Docket: 11675.165.2
PR0000000428V001

CERTIFICATE OF DEPOSIT UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 3rd day of December 2004.



Transmitted: REQUEST FOR A CERTIFICATE OF CORRECTION UNDER 37 C.F.R. § 1.323,
PTO Form 1050 and Check No. 139938 for \$100.00

DEC 29 2004

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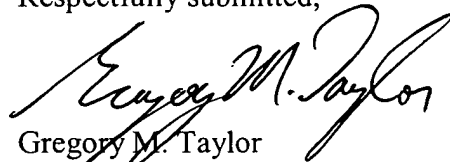
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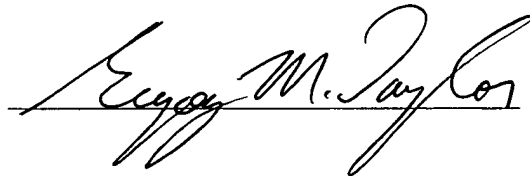


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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 6,790,762 B1
 DATED : September 14, 2004
 INVENTOR(S) : Zhiping Yin, Mark Jost

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Cover Page

Item 57, ABSTRACT, replace entire abstract with

—The present invention relates to the formation of an ILD layer while preventing of reducing oxidation of the upper surface of a metallic interconnect. Avoidance of oxidation of the upper surface of a metallic interconnect is achieved according to the present invention by passivating the exposed upper surface of the metallic interconnect prior to formation of the ILD. In order to avoid the oxidation of an upper surface of the interconnect during the formation of an ILD layer, an *in situ* passivation of the upper surface of the interconnect, immediately prior to or simultaneously with the formation of the ILD layer avoids the problems of the prior art.—

Column 2

Line 8, before "can range" insert --20--

Column 4

Line 2, change "inventing" to --inventive--

Line 48, after "14" change "is" to --are--

Line 57, after "thereof" insert --.--

Column 7

Line 50, after "deposition" change "toot" to --tool.--

Column 8

Line 10, after "carried" insert --out--

Line 49, change "ID" to --ILD--

Column 9

Line 9, after "layer" insert --14--

Line 18, change "on-limiting" to --non-limiting--

MAILING ADDRESS OF SENDER: Gregory M. Taylor
 WORKMAN NYDEGGER
 1000 Eagle Gate Tower
 60 East South Temple
 Salt Lake City, Utah 84111

PATENT NO. 6,790,762 B1

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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

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DATED : September 14, 2004

INVENTOR(S) : Zhiping Yin, Mark Jost

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Column 10

Line 56, change "absorbed onto" to --adsorbed onto said--

Column 11

Line 62, change "beating" to --heating--

Column 13

Line 17, remove [and;]

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